

ABSTRACT OF THE DISCLOSURE

An image pickup apparatus includes a solid-state image pickup element formed on a single semiconductor chip, the solid-state image pickup element including

5 photoelectric conversion units arranged two-dimensionally, a plurality of CCDs for transferring charges generated by the photoelectric conversion units arranged two-dimensionally, each of the plurality of CCDs being arranged correspondingly to each line of

10 photoelectric conversion units, a plurality of charge detection circuits for detecting the charges from the plurality of CCDs and supplying corresponding signal levels, each of the plurality of charge detection circuits being arranged correspondingly to each CCD, a

15 common output line to which signals from the plurality of charge detection circuits are sequentially output, a plurality of transfer transistors for transferring the signals from the plurality of charge detection circuits to the common output line, and a scanning circuit for

20 controlling the plurality of transfer transistors to sequentially output the signals from the plurality of charge detection circuits to the common output line.